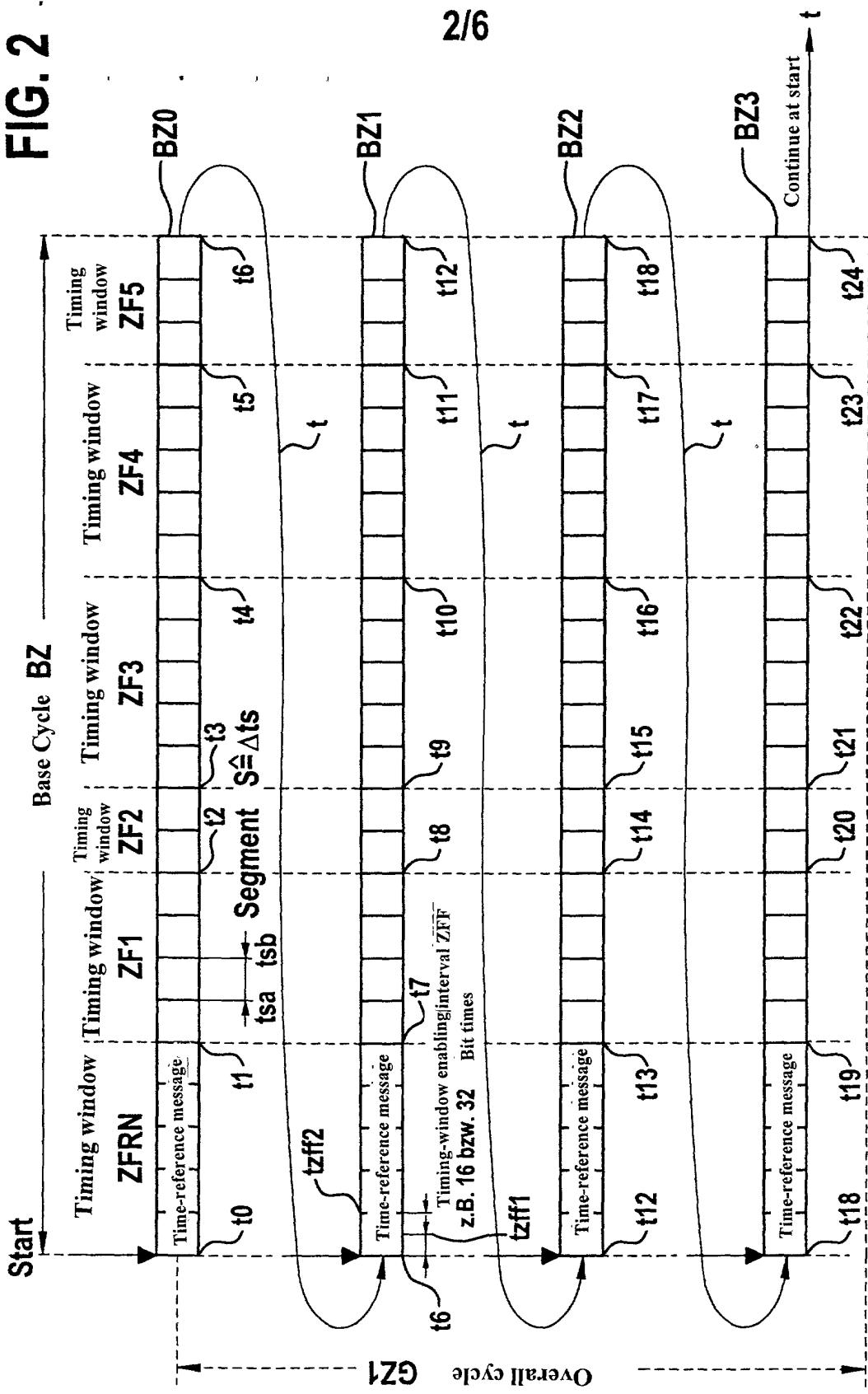
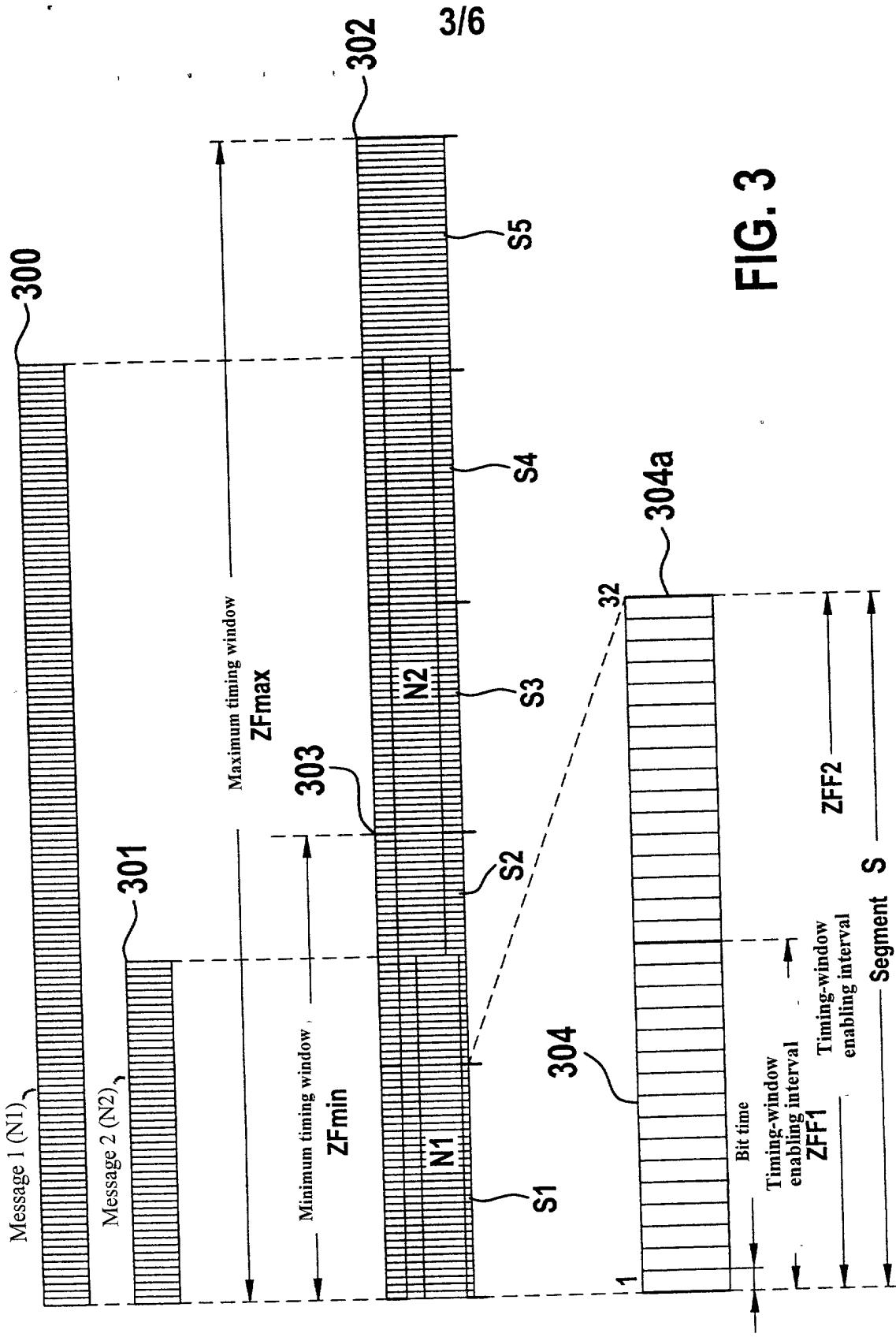


FIG. 1

**FIG. 2**

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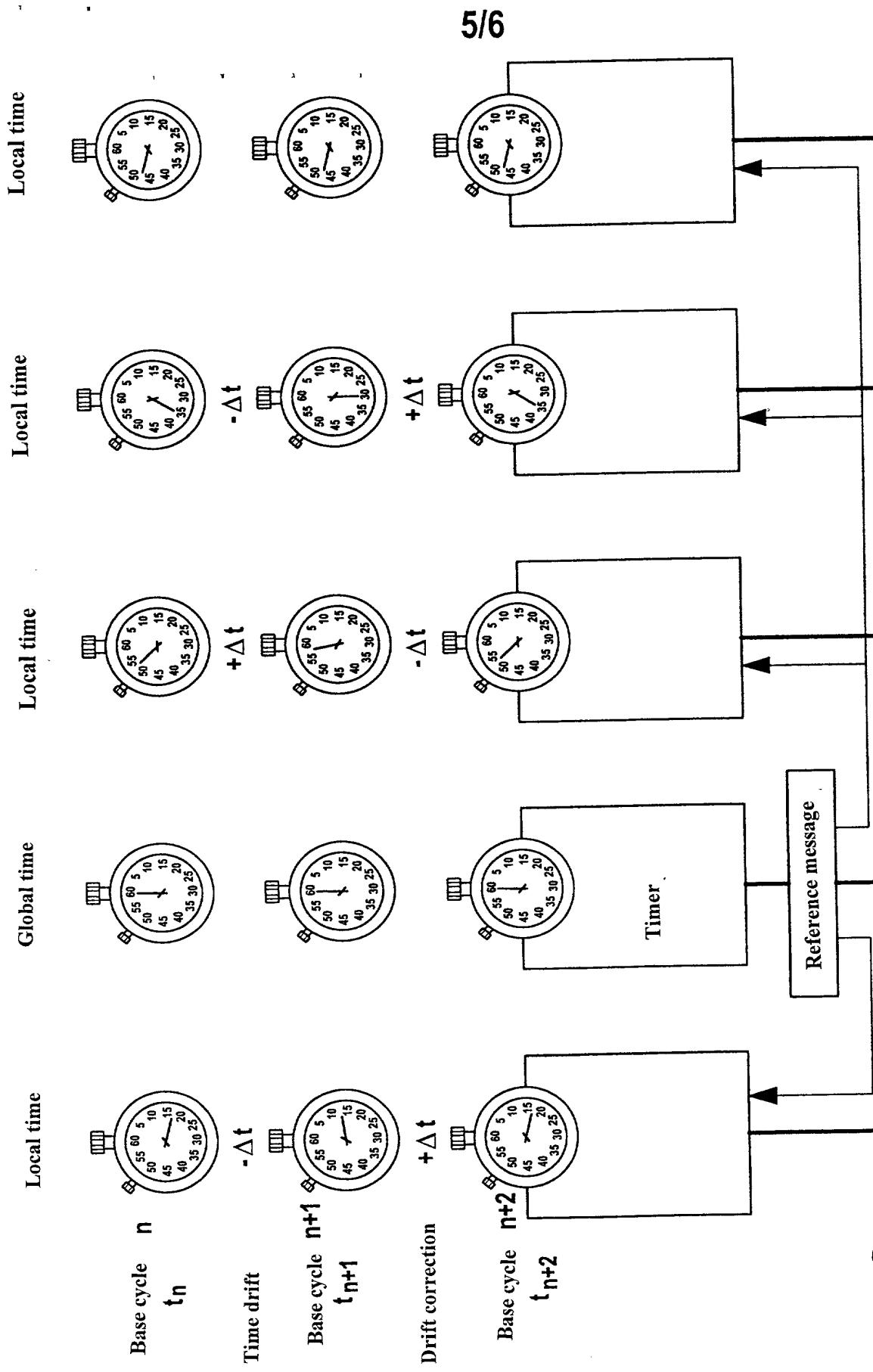
**FIG. 3**

**FIG. 4**

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Start cycle		Base cycle BZ							Overall cycle GZ		
		Timing window (Column in the matrix)		Timing window (Line in the matrix)		Timing window		Timing window			
Timing window (Column in the matrix)		ZF1a		ZF2a		ZF3a		ZF4a		ZF5a	
BZ0a	400	401	A	402	E	403	Arbitrating	404	A	405	B
BZ1a	406	407	A	408	E	409		410	A	411	C
BZ2a	412	413	A	414	E	415	D	416	A	417	
BZ3a	418	419	A	420	E	421		422	A	423	C
BZ4a	424	425	A	426	E	427	Arbitrating	428	A	429	B
BZ5a	430	431	A	432	E	433	F	434	A	435	C
BZ6a	436	437	A	438	E	439	D	440	A	441	
BZ7a	442	443	A	444	E	445		446	A	447	C

$A, F \triangleq$  Transmission groups  
 Arbitrating  $\triangleq$  reserved timing windows for arbitrating messages



**FIG. 5**

FIG. 6

Drift correction  
(Block diagram)